Docket No.: 50090-332

UTILITY PATENT APPLICATION _UNDER 37 CFR 1.53(b)

Box PATENT APPLICATION PE Commissioner for Patents
Washington, DC 20231
Sir:

Transmitted herewith for filing is the patent application of:

INVENTOR: Hisaya MORI, Shinji YAMADA, Teruhiko FUNAKURA

FOR: APPARATUS AND METHOD FOR TESTING SEMICONDUCTOR INTEGRATED CIRCUIT

Fnclos	sed are:		
\Box	38 pages of specification, claims, abstract	l. (3	
	Declaration and Power of Attorney.	•	
	Priority Claimed.		
	Certified copy of Japanese Patent Ap	plication No. 2001-032596	* * * * * * * * * * * * * * * * * * * *
	11 sheets of formal drawing.		
⊡	and	SUBISHI DENKI KABUSHIKI RYODEN SEMICONDUCTOR GINEERING CORPORATION	KAISHA, SYSTEM
	and the assignment recordation fee. An associate power of attorney. A verified statement to establish small en Information Disclosure Statement, Form Return Receipt Postcard	•	7 CFR 1.27.

The filing fee has been calculated as shown below:

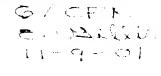
	NO. OF	1.23	EXTRA		
HINDS EN 1958AN	CLAIMS		CLAIMS	RATE	AMOUNT
Total Claims	6	-20	0	\$18.00	\$0.00
Independent Claims	2	-3	0	\$80.00	\$0.00
	Multiple Dependent Claim(s)				
	Basic Fee Total of Above Calculations				\$710.00
					\$710.00
			Less	½ for Small Entity	\$0.00
Assignment & Recording Fee					\$40.00
			SALE OF THE	of intelline	750:00



PATENT TRADEMARK OFFICE

-					
 □ □	Please charge my Deposit Account No. 500417 in the amount of \$750.00. A duplicate copy of this sheet is enclosed. The Commissioner is hereby authorized to charge payment of the following fees associated with this communication or credit any overpayment to Deposit Account No. 500417. A duplicate copy is enclosed. Any additional filing fees required under 37 CFR 1.16.				
<u> </u>	The Commissioner is hereby authorized to charge payment of the following fees during the pendency of this application or credit any overpayment to Deposit Account N 500417. A duplicate copy of this sheet is enclosed. • Any patent application processing fees under 37 CFR 1.17. • Any filing fees under 37 CFR 1.16 for presentation of extra claims.				
	Respectfully submitted,				
	MCDERMOTT, WILL & EMERY Stephen A. Becker				
	Registration No. 26,527				
Washi (202) Tate:	sth Street, N.W. ngton, DC 20005-3096 756-8000 SAB:ykg August 13, 2001 nile: (202) 756-8087				

OT 0 9 2001 2 50090-332



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Hisaya MORI, et al.

Serial No.: 09/927,368

Group Art Unit: 2812

Filed: August 13, 2001

Examiner:

For:

APPARATUS AND METHOD FOR TESTING SEMICONDUCTOR INTEGRATED

CIRCUIT

REQUEST FOR CORRECTED FILING RECEIPT

Commissioner for Patents Washington, DC 20231

Sir:

Attached is a copy of the Filing Receipt received from the U.S. Patent and Trademark Office in the above-referenced application. It is noted that the name of the second Assignee is incorrect on the filing receipt. Attached is a copy of the Transmittal Letter, which evidences that the name of the second Assignee should read: **RYODEN SEMICONDUCTOR SYSTEM ENGINEERING CORPORATION**. It is requested that a corrected filing receipt be issued.

Respectfully submitted,

MCDERMOTT, WILL & EMERY

Gene Z. Rubinson

Registration No. 33,351

600 13th Street, N.W. Washington, DC 20005-3096 (202) 756-8000 GZR:kjw

Date: October 8, 2001 Facsimile: (202) 756-8087



UNITED STATES PATENT AND TRADEMARK OFFICE

COMMISSIONER FOR PATENTS
UNITED STATES PATENT AND TRADEMARK OFFICE
WASHINGTON, D.C. 20231
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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY.DOCKET.NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
09/927,368	08/13/2001	2812	710	50090-332	11	6	2

CONFIRMATION NO. 4507

FILING RECEIPT

OC00000006566927

SEP 2 6 2001

McDERMOTT, WILL & EMERY 600 13th Street, N.W. Washington, DC 20005-3096

Date Mailed: 09/17/2001

Receipt is acknowledged of this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Customer Service Center. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Hisaya Mori', Hyogo, JAPAN; Shinji Yamada, Hyogo, JAPAN; Teruhiko Eunakura, Tokyo, JAPAN;

Assignment For Published Patent Application

MITSUBISHI DENKI KABUSHIKI KAISHA;

RYODEN SEMICONDUCTOR SYSTEM ENGINEERING CORRESPONDEN

Domestic Priority data as claimed by applicant

Foreign Applications

JAPAN 2001-032596 02/08/2001

If Required, Foreign Filing License Granted 09/17/2001

Projected Publication Date: 08/08/2002

Non-Publication Request: No

Early Publication Request: No

Title

Apparatus and method for testing semiconductor integrated circuit

Preliminary Class

438

Data entry by : BEKELE, TSEGEREDA

Team : OIPE

Date: 09/17/2001



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- The articles such as "a," "an" and "the" are not included as the first words in the title of an application. They are considered to be unnecessary to the understanding of the title.
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